

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)**Search Results -**

Term	Documents
32-BIT.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13545
32-BITS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1575
(7 AND 32-BIT).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	3

Database:

[US Patents Full-Text Database](#)
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[Derwent World Patents Index](#)
[IBM Technical Disclosure Bulletins](#)

Refine Search:

17 and '32-bit'

[Clear](#)**Search History****Today's Date: 10/1/2001**

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	17 and '32-bit'	3	L10
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	17 and network\$	1	L9
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	17 and ethernet	0	L8
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	(pattern\$1 and mask\$1 and ram\$1).ti,ab.	150	L7
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	15 and (mask\$ near5 group\$)	0	L6
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	14 and ram\$1	4	L5
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	13 and (mask\$ near5 data\$)	8	L4
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	12 and (pattern near5 data\$)	164	L3
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	11 and mac	1388	L2
USPT,PGPB,JPAB,EPAB,DWPI,TDBD	ethernet and lan	5581	L1

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Your wildcard search against 2000 terms has yielded the results below

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Search Results -

Term	Documents
PATTERNS	0
PATTERN.USPT.	424243
PATTERNA.USPT.	8
PATTERNAAND.USPT.	1
PATTERNABILITY.USPT.	37
PATTERNABLE.USPT.	361
PATTERNACTIVATED.USPT.	1
PATTERNANALYSIS.USPT.	1
PATTERNAND.USPT.	6
PATTERNAPPLICATION.USPT.	1
.....	
PATTERNS\$(PATTERN/SOG).USPT.	pickup term
(L1 AND (PATTERNS\$ NEAR5 MATCH\$)).USPT.	1

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Refine Search:

Search History

Today's Date: 10/1/2001

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	11 and (pattern\$ near5 match\$)	1	<u>L34</u>
USPT	(mask\$ near5 word\$1) same pattern\$	660	<u>L33</u>
USPT	18 and compar\$	1	<u>L32</u>
USPT	130 and '32-bit'	0	<u>L31</u>
USPT	5892768.pn.	1	<u>L30</u>
USPT	126 and (number\$ near5 pattern\$)	1	<u>L29</u>
USPT	126 and (interfac\$ near5 logic)	0	<u>L28</u>
USPT	126 and network\$	0	<u>L27</u>
USPT	124 and pattern\$1	1	<u>L26</u>
USPT	124 and (eight near5 pattern\$1)	0	<u>L25</u>
USPT	18 and '32-bit'	1	<u>L24</u>
USPT	6094443.uref.	0	<u>L23</u>
USPT	121 and ethernet	1	<u>L22</u>
USPT	119 and memory	34	<u>L21</u>
USPT	119 and 117	0	<u>L20</u>
USPT	'4-bit mask'	39	<u>L19</u>
USPT	117 and '4-bit mask'	0	<u>L18</u>
USPT	'32 mask bits'	33	<u>L17</u>
USPT	115 and bit\$1	1	<u>L16</u>
USPT	113 and memory	1	<u>L15</u>
USPT	113 and ram\$1	0	<u>L14</u>
USPT	110 and (mask\$)	1	<u>L13</u>
USPT	110 and (mask\$ same register\$1)	0	<u>L12</u>
USPT	110 and (mask\$ near5 register\$1)	0	<u>L11</u>
USPT	18 and pattern\$	1	<u>L10</u>
USPT	5778000.uref.	2	<u>L9</u>
USPT	5778000.pn.	1	<u>L8</u>
USPT	577800.pn.	0	<u>L7</u>
USPT	13 and mas\$	0	<u>L6</u>
USPT	13 and 'bit mask'	0	<u>L5</u>
USPT	13 and '4 bit mask'	0	<u>L4</u>
USPT	11 and '32-bit'	1	<u>L3</u>
USPT	5938771.uref.	2	<u>L2</u>
USPT	5938771.pn.	1	<u>L1</u>

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Search Results - Record(s) 1 through 4 of 4 returned.

☐ 1. Document ID: US 5428754 A

L14: Entry 1 of 4 File: USPT Jun 27, 1995

US-PAT-NO: 5428754

DOCUMENT-IDENTIFIER: US 5428754 A

TITLE: Computer system with clock shared between processors
executing separate instruction streams

DATE-ISSUED: June 27, 1995

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Baldwin; David R.	Weybridge	N/A	N/A	GBX

US-CL-CURRENT: 712/220; 712/30

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWC	Draw Desc	Image
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☐ 2. Document ID: US 5329630 A

L14: Entry 2 of 4 File: USPT Jul 12, 1994

US-PAT-NO: 5329630

DOCUMENT-IDENTIFIER: US 5329630 A

TITLE: System and method using double-buffer preview mode

DATE-ISSUED: July 12, 1994

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Baldwin; David R.	Weybridge	N/A	N/A	GBX

US-CL-CURRENT: 711/173; 711/149, 712/28

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWC	Draw Desc	Image
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☐ 3. Document ID: US 5056015 A

L14: Entry 3 of 4 File: USPT Oct 8, 1991

US-PAT-NO: 5056015
DOCUMENT-IDENTIFIER: US 5056015 A

TITLE: Architectures for serial or parallel loading of writable control store

DATE-ISSUED: October 8, 1991

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP	CODE	COUNTRY
Baldwin; David R.	Weybridge	N/A	N/A		GBX
Wilson; Malcolm E.	Salwayash	N/A	N/A		GBX
Trevett; Neil F.	Kingston-upon-Thames	N/A	N/A		GBX

US-CL-CURRENT: 703/27

Full	Title	Citation	Front	Review	Classification	Date	Reference
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KWIC	Draw Desc	Image
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☐ 4. Document ID: US 5042000 A

L14: Entry 4 of 4 File: USPT Aug 20, 1991

US-PAT-NO: 5042000
DOCUMENT-IDENTIFIER: US 5042000 A

TITLE: Integral transform method

DATE-ISSUED: August 20, 1991

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP	CODE	COUNTRY
Baldwin; David R.	Weybridge	N/A	N/A		GBX

US-CL-CURRENT: 708/404

Full	Title	Citation	Front	Review	Classification	Date	Reference
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KWIC	Draw Desc	Image
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Generate Collection

Term	Documents
ETHERNET.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	11888
ETHERNETS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	167
(13 AND ETHERNET).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	4

Display

50

Documents, starting with Document:

4

Display Format:

WEST[Generate Collection](#)**Search Results - Record(s) 1 through 4 of 4 returned.**☐ 1. Document ID: US 6108713 A

L5: Entry 1 of 4 File: USPT Aug 22, 2000

US-PAT-NO: 6108713

DOCUMENT-IDENTIFIER: US 6108713 A

TITLE: Media access control architectures and network
management systems

DATE-ISSUED: August 22, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Sambamurthy; Namakkal S.	San Jose	CA	N/A	N/A
Tripathi; Devendra K.	San Jose	CA	N/A	N/A
Deb; Alak K.	San Jose	CA	N/A	N/A
Truong; Linh Tien	San Jose	CA	N/A	N/A
Kumar; Praveen D.	Fremont	CA	N/A	N/A

US-CL-CURRENT: 709/250; 370/463

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWC	Draw Desc	Image
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☐ 2. Document ID: US 6085248 A

L5: Entry 2 of 4 File: USPT Jul 4, 2000

US-PAT-NO: 6085248

DOCUMENT-IDENTIFIER: US 6085248 A

TITLE: Media access control transmitter and parallel network management system

DATE-ISSUED: July 4, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Sambamurthy; Namakkal S.	San Jose	CA	N/A	N/A
Tripathi; Devendra K.	San Jose	CA	N/A	N/A
Deb; Alak K.	San Jose	CA	N/A	N/A
Truong; Linh Tien	San Jose	CA	N/A	N/A
Kumar; Praveen D.	Fremont	CA	N/A	N/A

US-CL-CURRENT: 709/229; 709/223, 709/230, 709/232, 709/250,
710/1, 710/20, 710/22, 710/5, 711/100

Full	Title	Citation	Front	Review	Classification	Date	Reference
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KIMC	Draw Desc	Image
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☐ 3. Document ID: US 6076115 A

L5: Entry 3 of 4

File: USPT

Jun 13, 2000

US-PAT-NO: 6076115

DOCUMENT-IDENTIFIER: US 6076115 A

TITLE: Media access control receiver and network management system

DATE-ISSUED: June 13, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Sambamurthy; Namakkal S.	San Jose	CA	N/A	N/A
Tripathi; Devendra K.	San Jose	CA	N/A	N/A
Deb; Alak K.	San Jose	CA	N/A	N/A
Truong; Linh Tien	San Jose	CA	N/A	N/A
Kumar; Praveen D.	Fremont	CA	N/A	N/A

US-CL-CURRENT: 709/250; 709/224, 709/230

Full	Title	Citation	Front	Review	Classification	Date	Reference
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KIMC	Draw Desc	Image
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☐ 4. Document ID: US 6044400 A

L5: Entry 4 of 4

File: USPT

Mar 28, 2000

US-PAT-NO: 6044400

DOCUMENT-IDENTIFIER: US 6044400 A

TITLE: Switch monitoring system having a data collection device using filters in parallel orientation and filter counter for counting combination of filtered events

DATE-ISSUED: March 28, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP	CODE	COUNTRY
Golan; Oded	Tel Aviv	N/A	N/A		ILX
Tivon; Rina	Rosh Ha'ain	N/A	N/A		ILX
Kruk; Meir	Ramat Gan	N/A	N/A		ILX
Lebovich; Gil	Tel Aviv	N/A	N/A		ILX
Goldenberg; Zvika	Petach Tikva	N/A	N/A		ILX
Pivonya; Gilad	Moshav Avichail	N/A	N/A		ILX
Perelman; Mark	Netanya	N/A	N/A		ILX
Dician; Yaron	Tel Aviv	N/A	N/A		ILX

US-CL-CURRENT: 709/224; 709/223

Full	Title	Citation	Front	Review	Classification	Date	Reference
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KIMC	Draw Desc	Image
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Term	Documents
RAM\$1	0
RAM.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	242499
RAMA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	877
RAMB.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	87
RAMC.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	41
RAMD.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	24
RAME.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	461
RAMF.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	11
RAMG.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	5
RAMH.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	3
(L4 AND RAM\$1).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	4

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Search Results - Record(s) 1 through 1 of 1 returned.

☐ 1. Document ID: DE 69319567 E, EP 605091 A2, EP 605091 A3, US 5657448 A, EP 605091 B1

L9: Entry 1 of 1

File: DWPI

Aug 13, 1998

DERWENT-ACC-NO: 1994-210331

DERWENT-WEEK: 199838

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TITLE: Operational configuration forming appts. for LAN interface board - has NVRAM, RAM and PROM on board and control code causing various functional routines to be selected to generate different board functions

INVENTOR: BARRETT, L F; KALWITZ, G A ; KRASLAVSKY, A J ;
RUSSELL, W C ; WADSWORTH, R D

PRIORITY-DATA: 1992US-0978286 (November 18, 1992)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 69319567 E	August 13, 1998	N/A	000	H04L029/06
EP 605091 A2	July 6, 1994	E	080	H04L029/06
EP 605091 A3	December 14, 1994	N/A	000	H04L029/06
US 5657448 A	August 12, 1997	N/A	063	G06F009/06
EP 605091 B1	July 8, 1998	E	000	H04L029/06

INT-CL (IPC): G06F 9/06; G06F 13/14; H04L 29/06

Full Title Citation Front Review Classification Date Reference

KWIC Draw Desc Clip Img Image

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Term	Documents
NETWORK\$	0
NETWORK.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	454402
NETWORKA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	6
NETWORKABILITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	7
NETWORKABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	85
NETWORKACCESSIBLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
NETWORKADDRESS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	7
NETWORKADDRESSSTRUCT.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
NETWORKANALOG.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
NETWORKANALYZER.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
(L7 AND NETWORK\$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	1

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1

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Search Results - Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 5900887 A

L10: Entry 1 of 3

File: USPT

May 4, 1999

US-PAT-NO: 5900887

DOCUMENT-IDENTIFIER: US 5900887 A

TITLE: Multiplexed wide interface to SGRAM on a graphics controller for complex-pattern fills without color and mask registers

DATE-ISSUED: May 4, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Leung; Clement K.	Fremont	CA	N/A	N/A
Ranganathan; Ravi	Cupertino	CA	N/A	N/A

US-CL-CURRENT: 345/531; 345/519, 345/563, 365/230.03

Full	Title	Citation	Front	Review	Classification	Date	Reference
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KWIC	Draw Desc	Image
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☐ 2. Document ID: US 5633661 A

L10: Entry 2 of 3

File: USPT

May 27, 1997

US-PAT-NO: 5633661

DOCUMENT-IDENTIFIER: US 5633661 A

TITLE: Video display control system having block write with opaque pattern control expansion

DATE-ISSUED: May 27, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Morse; Gary J.	Boca Raton	FL	N/A	N/A

US-CL-CURRENT: 345/549

Full	Title	Citation	Front	Review	Classification	Date	Reference
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KWIC	Draw Desc	Image
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☐ 3. Document ID: US 522214 A

L10: Entry 3 of 3

File: USPT

Jun 22, 1993

US-PAT-NO: 522214

DOCUMENT-IDENTIFIER: US 522214 A

TITLE: Image processing using a RAM and repeat
read-modify-write operation

DATE-ISSUED: June 22, 1993

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kobayashi; Yoshinao	Hiratsuka	N/A	N/A	JPX

US-CL-CURRENT: 345/530; 345/443, 345/543, 345/563

Full	Title	Citation	Front	Review	Classification	Date	Reference	KWIC	Draw Desc	Image
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Term	Documents
32-BIT.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13545
32-BITS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1575
(7 AND 32-BIT).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	3

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Search Results - Record(s) 1 through 1 of 1 returned.

☐ 1. Document ID: US 6094443 A

L22: Entry 1 of 1

File: USPT

Jul 25, 2000

US-PAT-NO: 6094443

DOCUMENT-IDENTIFIER: US 6094443 A

TITLE: Apparatus and method for detecting a prescribed pattern
in a data stream by selectively skipping groups of nonrelevant
data bytes

DATE-ISSUED: July 25, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dwork; Jeffrey Roy	San Jose	CA	N/A	N/A

US-CL-CURRENT: 370/510; 370/514, 370/515, 375/365, 375/366,
375/368

Full	Title	CIT.1	REV.1	CLS.1	REF.1	DRAW.1

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Term	Documents
ETHERNET.USPT.	9516
ETHERNETS.USPT.	153
(21 AND ETHERNET).USPT.	1

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